

Product Change Notices

PCN No.:20111101

Date: 11/01/2011

This is to inform you that AME8853 datasheet has been changed from Rev. A.01 to Rev. B.01. This notification is for your information and concurrence.

If you require data or samples to qualify this change, please contact AME, Inc. within 30 calendar days of receiving of this notification.

If we do not receive any response from you within 30 calendar days from the date of receiving of this notification, we will consider that you have accepted this PCN.

If you have any questions concerning this change, please contact:

PCN Originator:

Name: Daniel Lee

Email: Daniel_Lee@ame.com.tw

Expected 1st Device Shipment Date: 11/01/2011

Earliest Year/Work Week of Changed Product: 1145

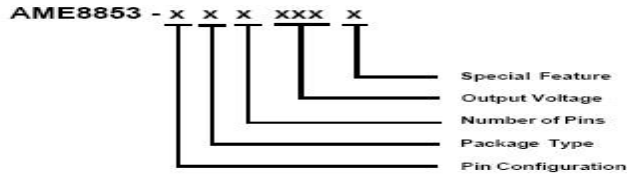
Modify Electrical Specifications of AME8853 as follows:

1. Cancel ADJ version.---page.5
2. Change Output Accuracy from $\pm 1.5\%$ to $\pm 2.0\%$.---page.8
3. Cancel Load regulation Test Condition "VIN=VOUT+1V" wording.---page.8
4. Change Enable Input Bias Current Test Condition as follows:
 - a. $V_{EN}=V_{IN}$, I_{EN_HI} , Max. 0.5uA ---page.9
 - b. $V_{EN}=0$, I_{EN_LO} , Max. 1.5uA. ---page.9

Electrical Specifications on Page. 5/ 8/ 9.

From

■ Ordering Information



Pin Configuration	Package Type	Number of Pins	Output Voltage	Special Feature
A 1. IN (SC-70-5) 2. GND 3. EN 4. NC 5. OUT	E: SOT-2X I: SC-70 (SC-82)	T: 3 U: 4 V: 5	100: 1.0V 110: 1.1V 120: 1.2V 130: 1.3V 140: 1.4V 150: 1.5V : : 430: 4.2V 430: 4.3V ADJ--Adjustable	N/A: SOT-2X K: 0.9mm max height (for TSOT-2XA Only)
B 1-IN (SC-70-6) 2-GND 3-EN 4-ADJ 5-OUT				
A 1. IN (SOT-25) (TSOT-25A) 2. GND 3. EN 4. NC 5. OUT				
B 1-IN (SOT-25) (TSOT-25A) 2-GND 3-EN 4-ADJ 5-OUT				
A 1. EN (SC-70-4) (SC-82) 2. GND 3. OUT 4. IN				
A 1. GND (SOT-23) 2. OUT 3. IN				

1. ---page 5

Parameter	Symbol	Test Condition	Min	Typ	Max	Units	
Output Accuracy (For Fixed Versions)	V _{OUT,ACC}	I _{OUT} =1mA	0.8V ≤ V _{OUT(NOM)} ≤ 1.2V	-2.0		2.0	%
			1.2V < V _{OUT(NOM)}	-1.5		1.5	

2. ---page.8

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Load Regulation $\frac{\Delta V_{OUT}}{V_{OUT}} \times 100\%$ $\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	REG _{LOAD}	V_{IN} = V_{OUT} + 4V 1mA ≤ I _{OUT} ≤ 300mA 0.8V ≤ V _{OUT(NOM)} ≤ 1.2V	-6.0		5.0	% / A
		V_{IN} = V_{OUT} + 4V 1mA ≤ I _{OUT} ≤ 300mA 1.2V ≤ V _{OUT(NOM)} ≤ 2.0V	-4.0		4.0	
		V_{IN} = V_{OUT} + 4V 1mA ≤ I _{OUT} ≤ 300mA 2.0V < V _{OUT(NOM)}	-3.0		3.0	

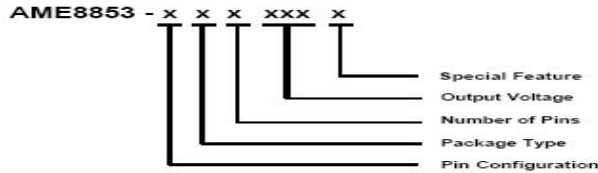
3. ---page.8

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Enable Input Bias Current	I _{EN}	V _{EN} =Floating			0.5	μA
		V _{EN} Pull-High (5MΩ built-in)		+		
		V _{EN} Pull-Low (5MΩ built-in)		+		

4. ---page.9

To

■ Ordering Information



Pin Configuration	Package Type	Number of Pins	Output Voltage	Special Feature
A (SC-70-6) 1. IN 2. GND 3. EN 4. NC 5. OUT	E: SOT-2X I: SC-70 (SC-82)	T: 3 U: 4 V: 6	080: 0.8V 090: 0.9V 100: 1.0V 110: 1.1V 120: 1.2V 130: 1.3V 140: 1.4V 150: 1.5V : : 420: 4.2V 430: 4.3V	N/A: SOT-2X K: 0.9mm max height (for TSOT-2XA Only)
A (SOT-23) (TSOT-23A) 1. IN 2. GND 3. EN 4. NC 5. OUT				
A (SC-70-4F) (SC-82) 1. EN 2. GND 3. OUT 4. IN				
A (SOT-23) 1. GND 2. OUT 3. IN				

1. ---page.5

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Output Accuracy	$V_{OUT,ACC}$		-2.0		2.0	%

2. ---page.8

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Load Regulation $\frac{\Delta V_{OUT}}{V_{OUT}} \times 100\%$ $\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	REG _{LOAD}	$1mA \leq I_{OUT} \leq 300mA$ $0.8V \leq V_{OUT(NOM)} \leq 1.2V$	-5.0		5.0	% / A
		$1mA \leq I_{OUT} \leq 300mA$ $1.2V \leq V_{OUT(NOM)} \leq 2.0V$	-4.0		4.0	
		$1mA \leq I_{OUT} \leq 300mA$ $2.0V < V_{OUT(NOM)}$	-3.0		3.0	

3. ---page.8

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Enable Input Bias Current	$I_{EN,HI}$	$V_{EN} = V_{IN}$			0.5	μA
	$I_{EN,LO}$	$V_{EN} = 0V$			1.5	

4. ---page.9

Reason for Change:

Comply with product performance.

■ General Description

The AME8853 family of positive, CMOS linear regulators provide low dropout voltage(300mV@300mA), low quiescent current, and low noise CMOS LDO. These rugged devices have both Thermal Shutdown, and Current limit to prevent device failure under the "Worst" of operating conditions.

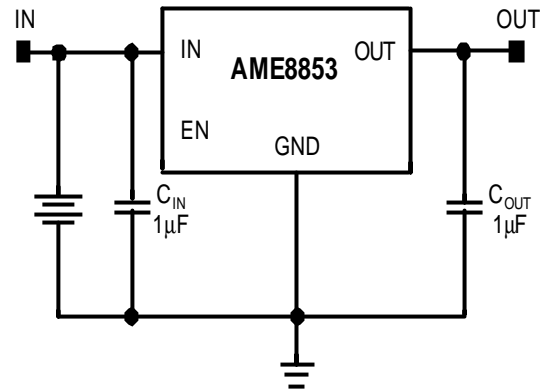
■ Features

- Low Dropout Voltage: 300mV@300mA
- Guaranteed Current: 300mA
- Quiescent Current: 60μA (typ.)
- Over-Temperature Shutdown
- Current Limiting protection
- PSRR: 60dB@10KHz
- Ultra-Low-Noise: 60μV_{RMS} at 10Hz to 100KHz
- Low Temperature Coefficient
- Input Voltage Range : 2.5V~5.5V
- Output Voltage Range: 0.8V~ 4.3V
- Green Products Meet RoHS Standards

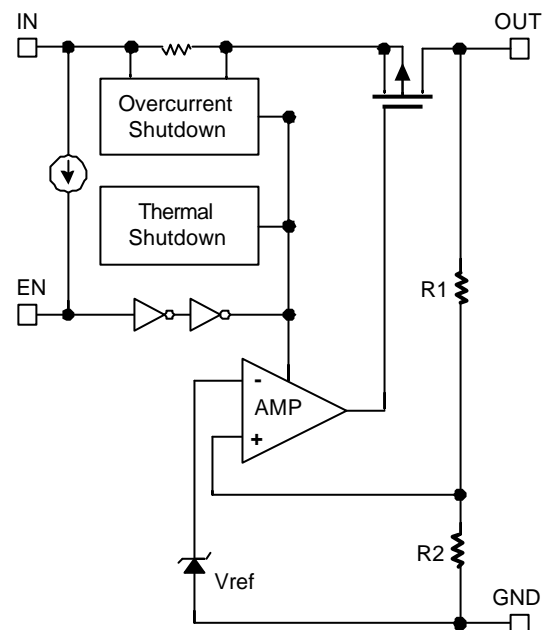
■ Applications

- Instrumentation
- Portable Electronics
- Wireless Devices
- Cordless Phones
- PC Peripherals
- Battery Powered Widgets

■ Typical Application

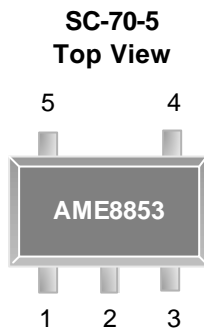


■ Functional Block Diagram



AME8853

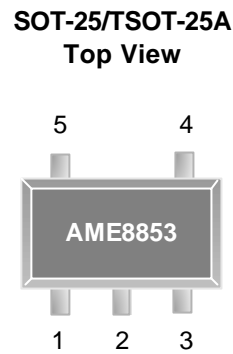
■ Pin Configuration



AME8853-AIVxxx

1. IN
2. GND
3. EN
4. NC
5. OUT

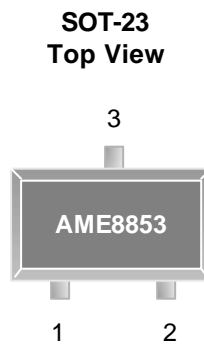
* Die Attach:
Conductive Epoxy



AME8853-AEVxxxx

1. IN
2. GND
3. EN
4. NC
5. OUT

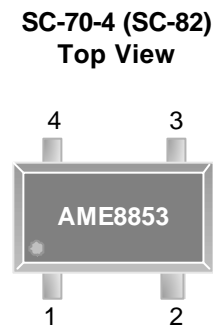
* Die Attach:
Conductive Epoxy



AME8853-AETxxx

1. GND
2. OUT
3. IN

* Die Attach:
Non-Conductive Epoxy



AME8853-AIUxxx

1. EN
2. GND
3. OUT
4. IN

* Die Attach:
Conductive Epoxy



AME8853

High PSRR, Low Noise, 300mA CMOS Regulator

■ Pin Description

SC-70-5

Pin Number	Pin Name	Pin Description
1	IN	Input voltage pin; should be decoupled with 1 μ F or greater capacitor.
2	GND	Ground connection pin.
3	EN	Enable pin, Active "high". When pulled "low", the PMOS pass transistor turns off, current consuming less than 1 μ A. When EN pin float outside, it's weakly pulled high.
4	NC	No connection.
5	OUT	LDO voltage regulator output pin; should be decoupled with a 1.0 μ F or greater value low ESR ceramic capacitor.

SOT-25/TSOT-25A

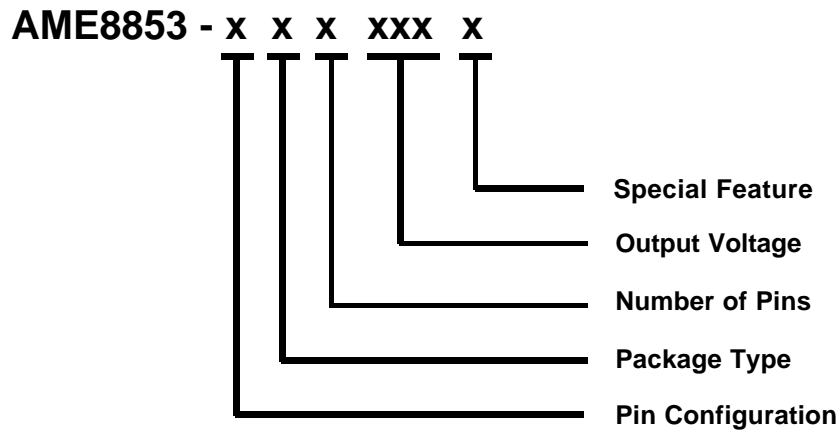
Pin Number	Pin Name	Pin Description
A		
1	IN	Input voltage pin; should be decoupled with 1 μ F or greater capacitor.
2	GND	Ground connection pin.
3	EN	Enable pin, Active "high". When pulled "low", the PMOS pass transistor turns off, current consuming less than 1 μ A. When EN pin float outside, it's weakly pulled high.
4	NC	No connection.
5	OUT	LDO voltage regulator output pin; should be decoupled with a 1.0 μ F or greater value low ESR ceramic capacitor.

AME8853**■ Pin Description****SOT-23**

Pin Number	Pin Name	Pin Description
1	GND	Ground connection pin.
2	OUT	LDO voltage regulator output pin; should be decoupled with a 1.0 μ F or greater value low ESR ceramic capacitor.
3	IN	Input voltage pin; should be decoupled with 1 μ F or greater capacitor.

SC-70-4(SC-82)

Pin Number	Pin Name	Pin Description
1	EN	Enable pin, Active "high". When pulled "low", the PMOS pass transistor turns off, current consuming less than 1 μ A. When EN pin float outside, it's weakly pulled high.
2	GND	Ground connection pin.
3	OUT	LDO voltage regulator output pin; should be decoupled with a 1.0 μ F or greater value low ESR ceramic capacitor.
4	IN	Input voltage pin; should be decoupled with 1 μ F or greater capacitor.

AME8853
■ Ordering Information


Pin Configuration	Package Type	Number of Pins	Output Voltage	Special Feature
A (SC-70-5) 1. IN 2. GND 3. EN 4. NC 5. OUT	E: SOT-2X I: SC-70 (SC-82)	T: 3 U: 4 V: 5	080: 0.8V 090: 0.9V 100: 1.0V 110: 1.1V 120: 1.2V 130: 1.3V 140: 1.4V 150: 1.5V : : : : 420: 4.2V 430: 4.3V	N/A: SOT-2X K: 0.9mm max height (for TSOT-2XA Only)
A (SOT-25) (TSOT-25A) 1. IN 2. GND 3. EN 4. NC 5. OUT				
A (SC-70-4/ SC-82) 1. EN 2. GND 3. OUT 4. IN				
A (SOT-23) 1. GND 2. OUT 3. IN				

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■ Absolute Maximum Ratings

Parameter	Symbol	Maximum	Unit
Input Voltage	V_{IN}	-0.3 to 6	V
EN Voltage	V_{EN}	-0.3 to 6	V
Output Current	I_{OUT}	$P_D/(V_{IN}-V_{OUT})$	mA
Output Voltage	V_{OUT}	GND-0.3 to $V_{IN}+0.3$	V
ESD Classification		B*	

■ Recommended Operating Conditions

Parameter	Symbol	Rating	Unit
Ambient Temperature Range	T_A	- 40 to +85	°C
Junction Temperature Range	T_J	- 40 to +125	°C
Storage Temperature Range	T_{STG}	- 65 to +150	°C

■ Thermal Information

Parameter	Package	Die Attach	Symbol	Maximum	Unit
Thermal Resistance* (Junction to Case)	SC-70-5	Conductive Epoxy	θ_{JC}	N/A	°C / W
	SC-70-4(SC-82)			81	
	SOT-25			81	
	TSOT-25A			140	
	SOT-23	Non-Conductive Epoxy		140	
Thermal Resistance (Junction to Ambient)	SC-70-5	Conductive Epoxy	θ_{JA}	331	°C / W
	SC-70-4(SC-82)			260	
	SOT-25			230	
	TSOT-25A			280	
	SOT-23	Non-Conductive Epoxy		280	
Internal Power Dissipation	SC-70-5	Conductive Epoxy	P_D	300	mW
	SC-70-4(SC-82)			400	
	SOT-25			455	
	TSOT-25A			400	
	SOT-23	Non-Conductive Epoxy		400	
Solder Iron (10Sec)**				350	°C

* Measure θ_{JC} on backside center of molding compound if IC has no tab.

** MIL-STD-202G 210F

AME8853

■ Electrical Specifications

$V_{IN} = V_{OUT(NOM)} + 1V$, (for $V_{OUT} < 2V$, $V_{IN} = 2.5V$), $I_{OUT} = 1mA$, and $C_{OUT} = 1\mu F$, $C_{IN} = 1\mu F$ unless otherwise noted.
Typical values are at $T_A = 25^\circ C$.

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Input Voltage	V_{IN}		2.5		5.5	V
Output Accuracy	$V_{OUT,ACC}$		-2.0		2.0	%
Output Voltage Range	V_{OUT}		0.8		4.3	V
Dropout Voltage (Note 1)	V_{DROP}	$I_{OUT} = 300mA$, $0.8V < V_{OUT(NOM)} < 2.0V$			Note2	mV
		$I_{OUT} = 300mA$, $2.0V < V_{OUT(NOM)} < 2.4V$			500	
		$I_{OUT} = 300mA$, $2.4V < V_{OUT(NOM)} < 2.8V$			400	
		$I_{OUT} = 300mA$, $2.8V < V_{OUT(NOM)} < 4.3V$		220	300	
Output Current	I_{OUT}		300			mA
Quiescent Current	I_Q	$I_{OUT} = 0mA$		60	90	μA
Line Regulation $\frac{\Delta V_{OUT}}{\Delta V_{IN}} \times 100\%$ $\frac{\Delta V_{OUT}}{V_{OUT}}$	REG_{LINE}	$I_{OUT} = 1mA$, $0.8V < V_{OUT} < 1.2V$, $2.5V < V_{IN} < 3.5V$	-0.25		0.25	% / V
		$I_{OUT} = 1mA$, $1.2V < V_{OUT} < 2.0V$, $2.5V < V_{IN} < 3.5V$	-0.2		0.2	
		$I_{OUT} = 1mA$, $2.0V < V_{OUT} < 4.2V$, $V_{IN(MIN)} < V_{IN} < V_{IN(MIN)} + 1V$	-0.1		0.1	
		$I_{OUT} = 1mA$, $4.2V < V_{OUT} < 4.5V$, $V_{IN(MIN)} < V_{IN} < 5.5V$ (Note2)	-0.25		0.25	
Load Regulation $\frac{\Delta V_{OUT}}{V_{OUT}} \times 100\%$ $\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	REG_{LOAD}	$1mA < I_{OUT} < 300mA$ $0.8V < V_{OUT(NOM)} < 1.2V$	-5.0		5.0	% / A
		$1mA < I_{OUT} < 300mA$ $1.2V < V_{OUT(NOM)} < 2.0V$	-4.0		4.0	
		$1mA < I_{OUT} < 300mA$ $2.0V < V_{OUT(NOM)} < 4.3V$	-3.0		3.0	

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■ Electrical Specifications (Contd.)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units	
Power Supply Rejection Ratio	PSRR	$C_{OUT}=1\mu F, V_{OUT}=0.8V$ $I_{OUT}=10mA$ $V_{IN}=2.8V$	F=100Hz		60		dB
			F=1KHz		60		
			F=10KHz		60		
Output Voltage Noise	eN	$I_{OUT}=10mA, V_{OUT}=0.8V,$ $f=10Hz$ to 100KHz		60		μV_{RMS}	
Enable High(enabled)	$V_{EN,HI}$	$V_{IN(MIN)}$ V_{IN} 5.5V	1.4		V_{IN}	V	
Enable Low (shutdown)	$V_{EN,LO}$	$V_{IN(MIN)}$ V_{IN} 5.5V	0		0.4	V	
Enable Input Bias Current	$I_{EN,HI}$	$V_{EN} = V_{IN}$			0.5	μA	
	$I_{EN,LO}$	$V_{EN} = 0V$			1.5		
Shutdown Current	I_{SHDN}	$V_{IN}=5.0V, V_{EN}=0V$		0.1	1	μA	
Shutdown Output Voltage	$V_{OUT,SD}$	$I_{OUT}=0.4mA, V_{EN}=0$			0.4	V	
Protection							
Output Current Limit	I_{LIM}	$V_{OUT}=0.9 \times V_{OUT(NOM)}$	350			mA	
Short-Circuit Current	I_{SC}	V_{OUT} 0.6V		150	300		
Thermal Shutdown Temperature	T_{SHDN}	Shutdown, temperature increasing		150		$^{\circ}C$	
Thermal Shutdown Hysteresis	$T_{SHDN(HYS)}$			20			

Note1: Dropout Voltage is measured at $V_{OUT}=V_{OUT(NOM)} \times 98\%$

Note2: For V_{OUT} below 2.0V, Dropout Voltage is the input_(MIN) to output differential.

AME8853

■ Detailed Description

The AME8853 family of CMOS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection thermal shutdown, and Power Good detection circuitry.

The P-channel pass transistor receives data from the error amplifier, over-current shutdown, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150°C, or the current exceeds 300mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops more 20°C.

Capacitor Selection and Regulator Stability

The AME8853 is stable with an output capacitor to ground of 1μF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1μF ceramic capacitor with a 10μF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost.

A second capacitor is recommended between the input and ground to stabilize V_{IN} . The input capacitor should be at least 0.1μF to have a beneficial effect.

Enable Pin

The Enable Pin is normally pull-high. When activated pulled low, the MOS pass transistor shuts off, and all internal circuits are powered down. In this state, the stand by current is less than 1μA.

Capacitor Selection and Regulator Stability

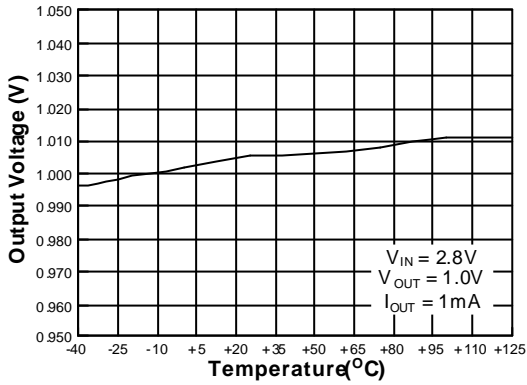
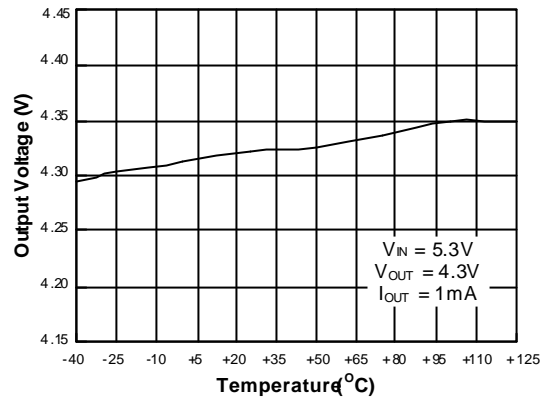
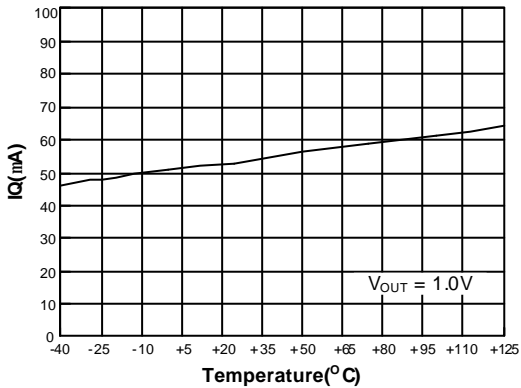
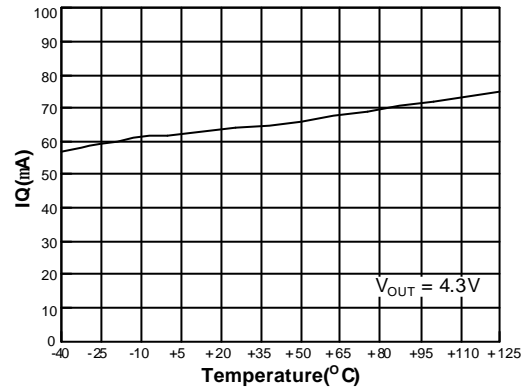
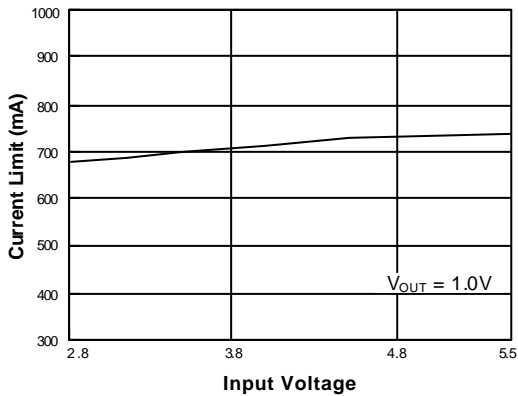
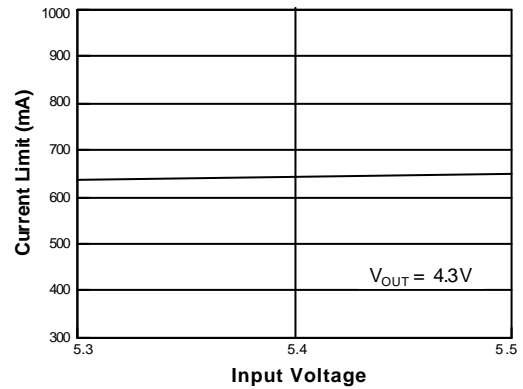
The maximum output power of the AME8853 is limited by the maximum power dissipation of the package. By calculation the power dissipation of the package as a function of the input voltage, output voltage and output current, the maximum input voltage can be obtained. The maximum power dissipation should not exceed the package' s maximum power rating.

$$P_{MAX} = (V_{IN(MAX)} - V_{OUT}) \times I_{OUT}$$

Where:

$V_{IN(MAX)}$ = maximum input voltage

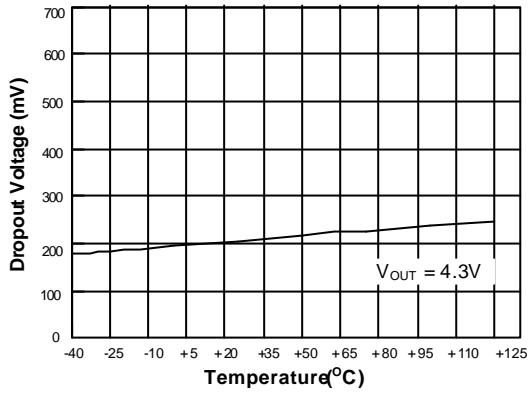
P_{MAX} = maximum power dissipation of the package

■ Characterization Curve
Output Voltage

Output Voltage

Quiescent Current

Quiescent Current

Current Limit vs. Input Voltage

Current Limit vs. Input Voltage


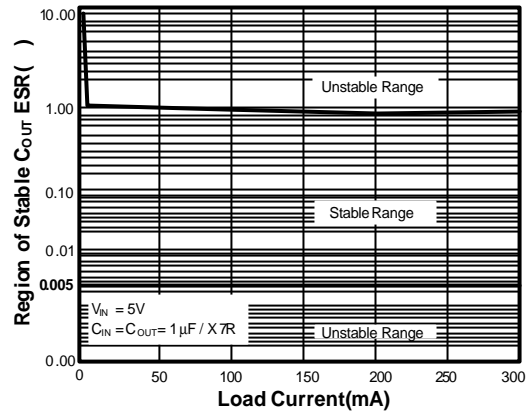
AME8853

■ Characterization Curve

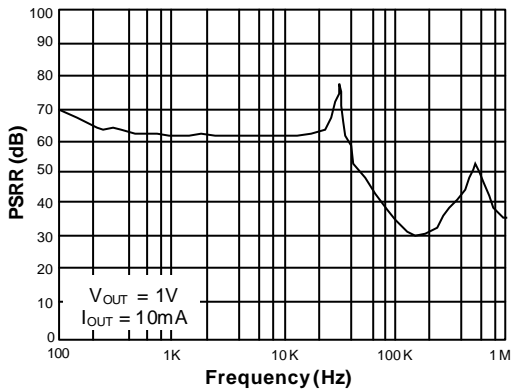
Dropout Voltage



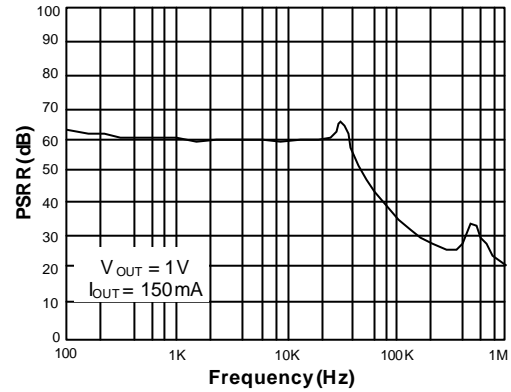
Stability vs. ESR vs. Load Current



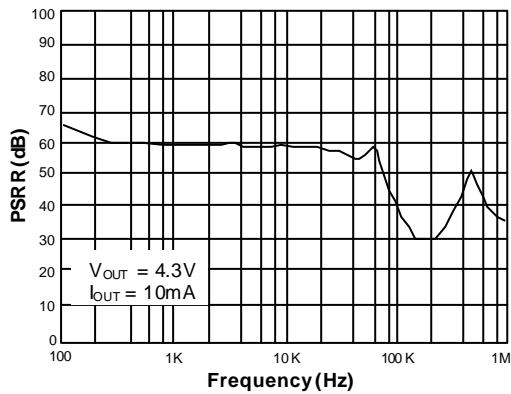
PSRR vs. Frequency



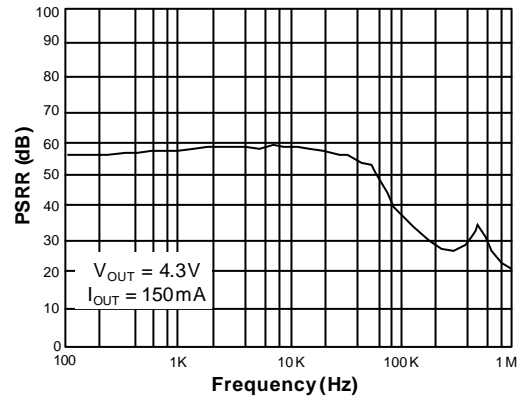
PSRR vs. Frequency



PSRR vs. Frequency



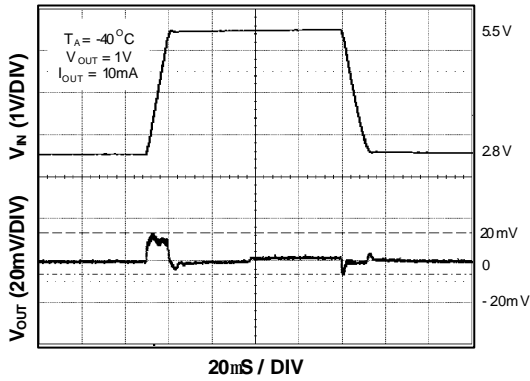
PSRR vs. Frequency



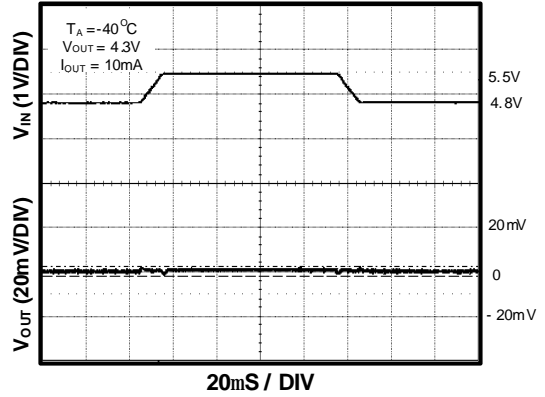
AME8853

■ Characterization Curve

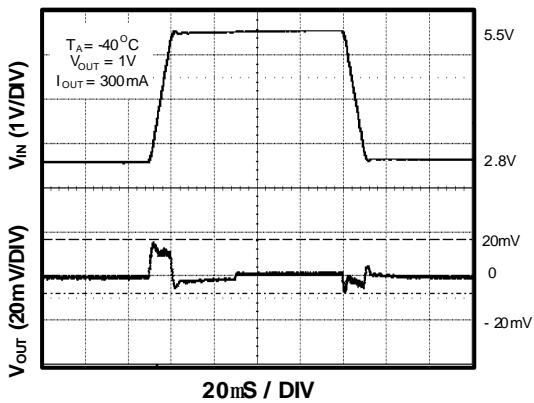
Line Transient Response



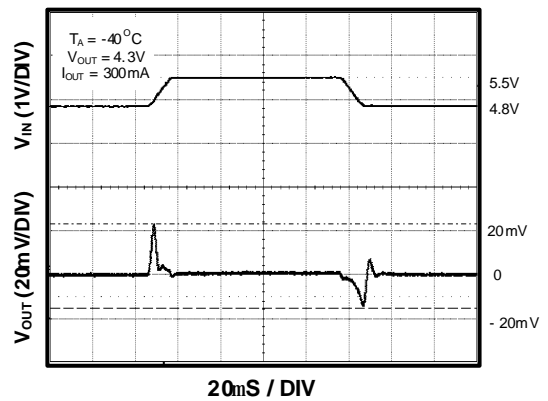
Line Transient Response



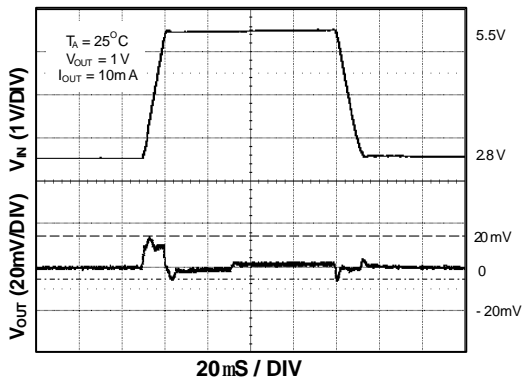
Line Transient Response



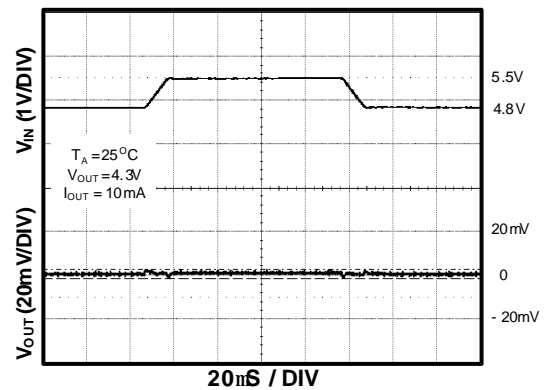
Line Transient Response

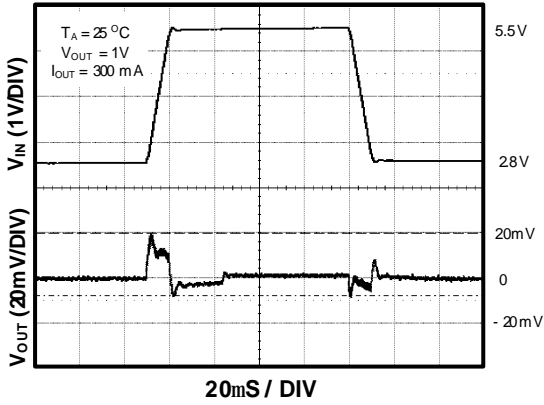
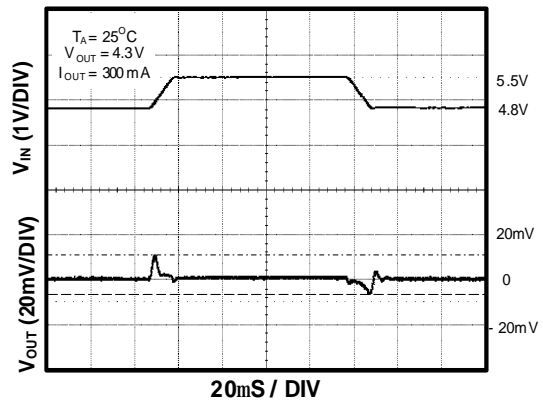
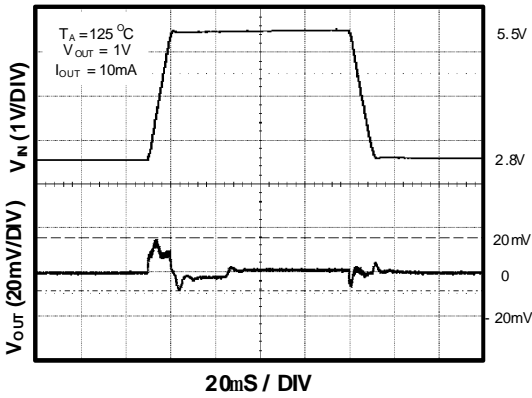
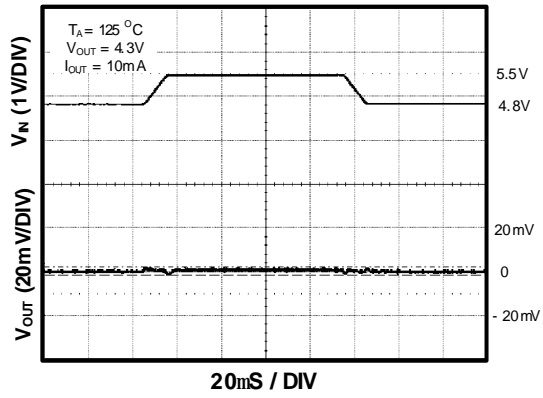
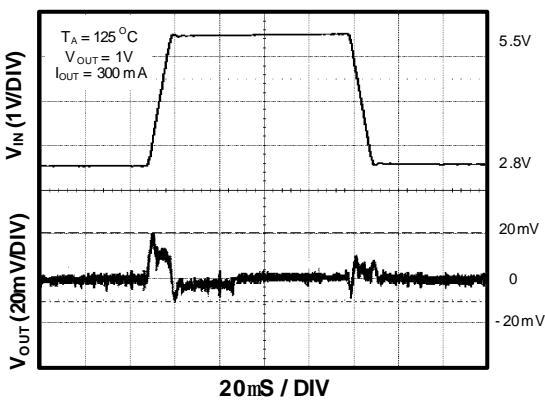
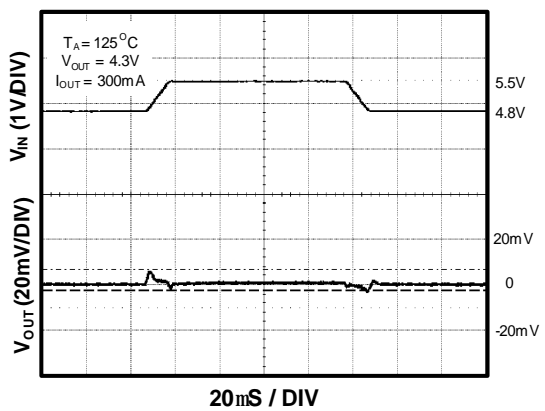


Line Transient Response



Line Transient Response

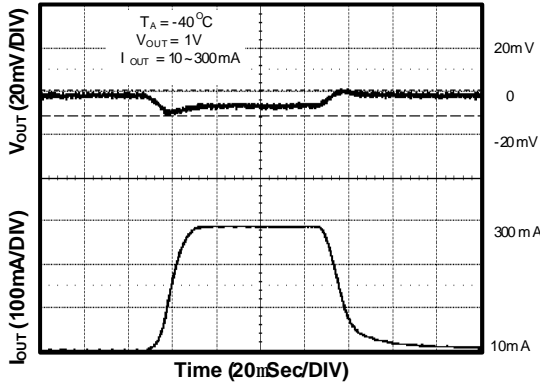


Line Transient Response

Line Transient Response

Line Transient Response

Line Transient Response

Line Transient Response

Line Transient Response


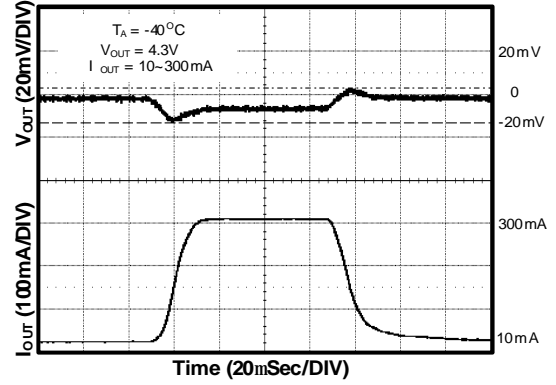
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■ Characterization Curve

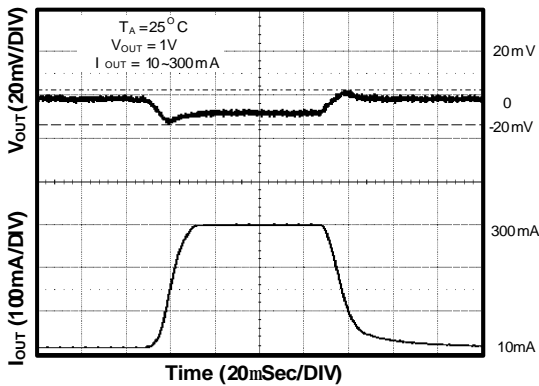
Load Transient Response



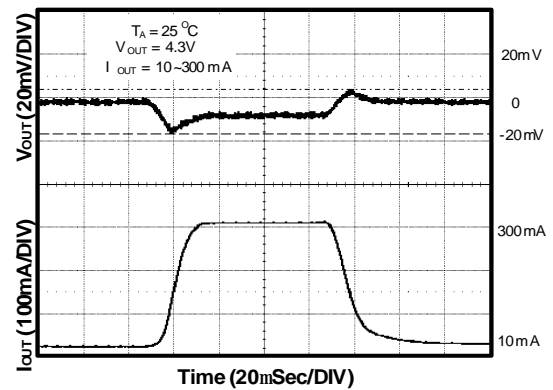
Load Transient Response



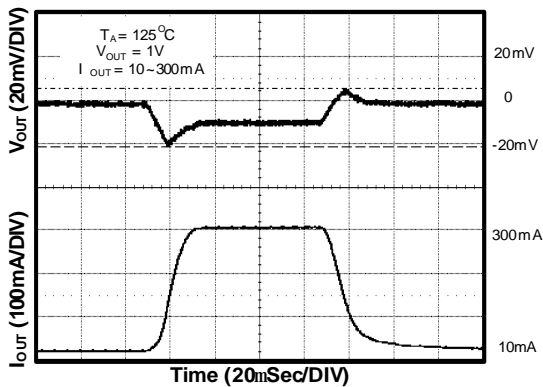
Load Transient Response



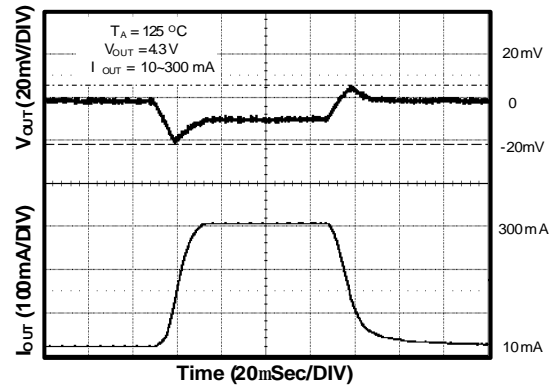
Load Transient Response



Load Transient Response



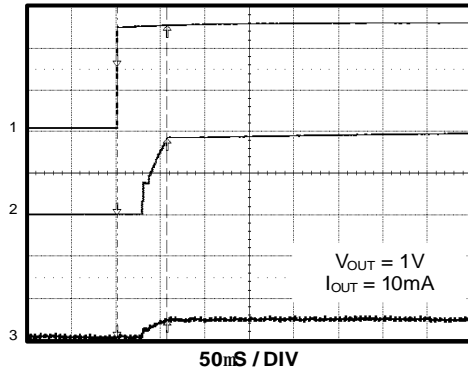
Load Transient Response



AME8853

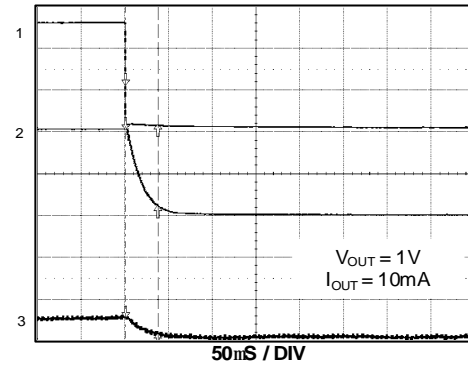
■ Characterization Curve

Chip Enable Transient Response



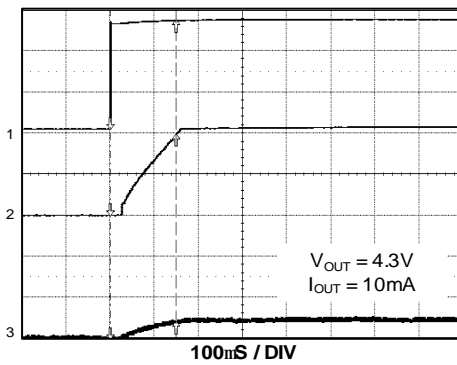
- 1) EN= 1V/div
- 2) V_{OUT} = 500mV/div
- 3) I_{OUT} = 20mA/div

Chip Enable Transient Response



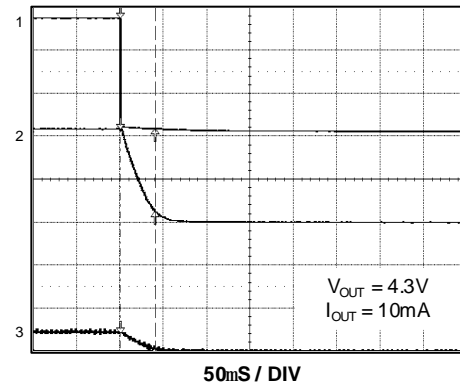
- 1) EN= 1V/div
- 2) V_{OUT} = 500V/div
- 3) I_{OUT} = 20mA/div

Chip Enable Transient Response



- 1) EN= 2V/div
- 2) V_{OUT} = 2V/div
- 3) I_{OUT} = 20mA/div

Chip Enable Transient Response

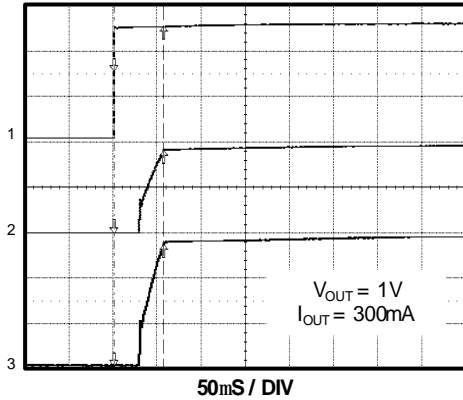


- 1) EN= 2V/div
- 2) V_{OUT} = 2V/div
- 3) I_{OUT} = 20mA/div

AME8853

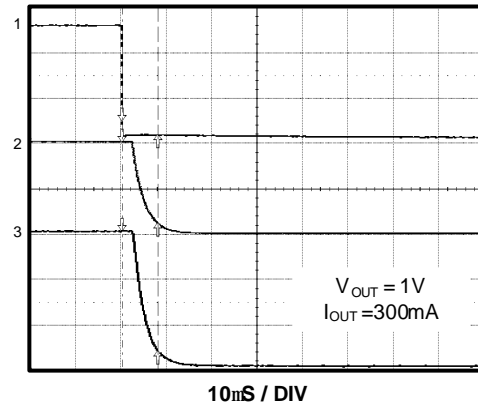
■ Characterization Curve

Chip Enable Transient Response



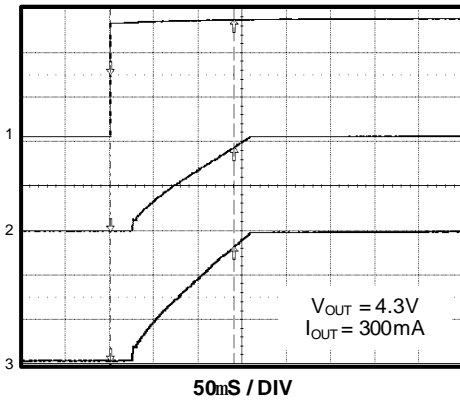
- 1) EN= 1V/div
- 2) $V_{OUT} = 500mV/div$
- 3) $I_{OUT} = 100mA/div$

Chip Enable Transient Response



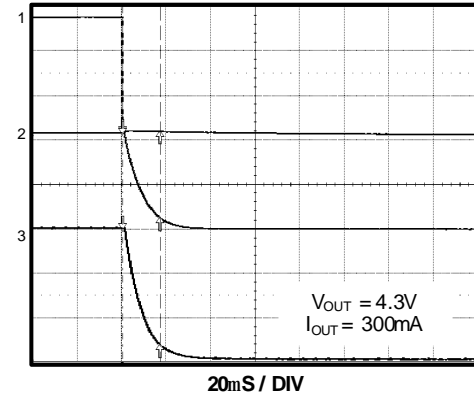
- 1) EN= 1V/div
- 2) $V_{OUT} = 500mV/div$
- 3) $I_{OUT} = 100mA/div$

Chip Enable Transient Response

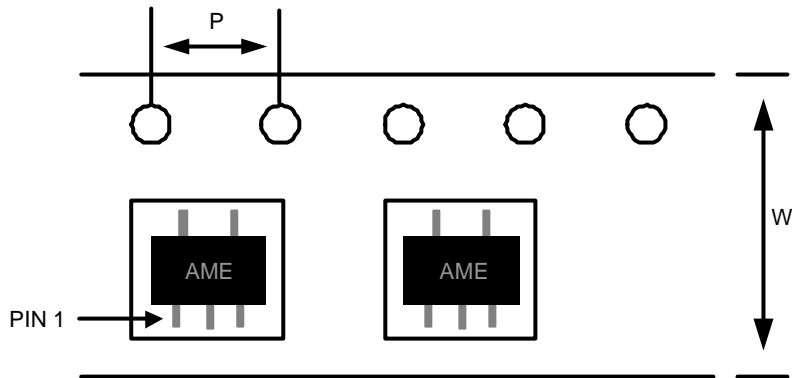


- 1) EN= 2V/div
- 2) $V_{OUT} = 2V/div$
- 3) $I_{OUT} = 100mA/div$

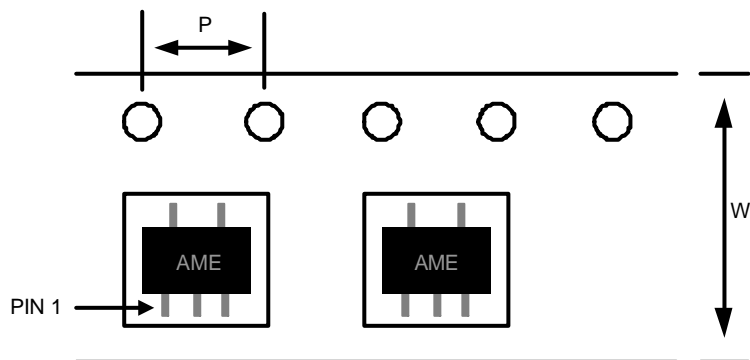
Chip Enable Transient Response



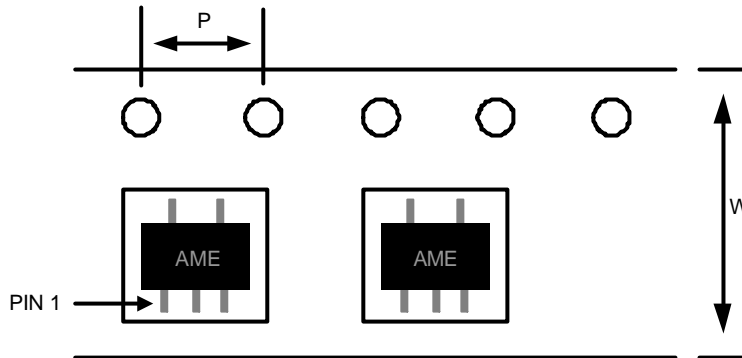
- 1) EN= 2V/div
- 2) $V_{OUT} = 2V/div$
- 3) $I_{OUT} = 100mA/div$

AME8853
■ Tape and Reel Dimension
SC-70-5

Carrier Tape, Number of Components Per Reel and Reel Size

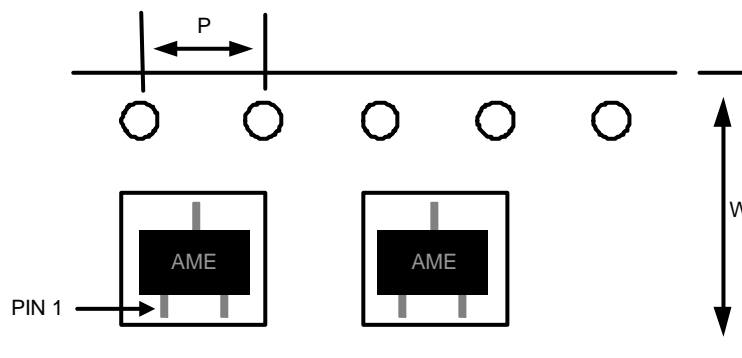
Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
SC-70-5	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

SOT-25

Carrier Tape, Number of Components Per Reel and Reel Size

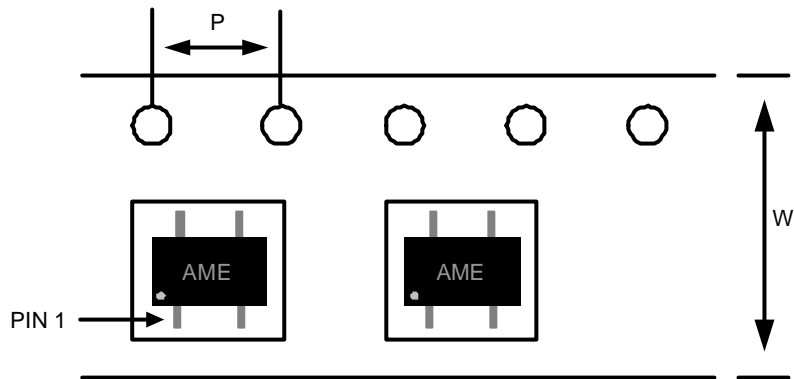
Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
SOT-25	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

AME8853
■ Tape and Reel Dimension (Contd.)
TSOT-25A

Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
TSOT-25A	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

SOT-23

Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
SOT-23	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

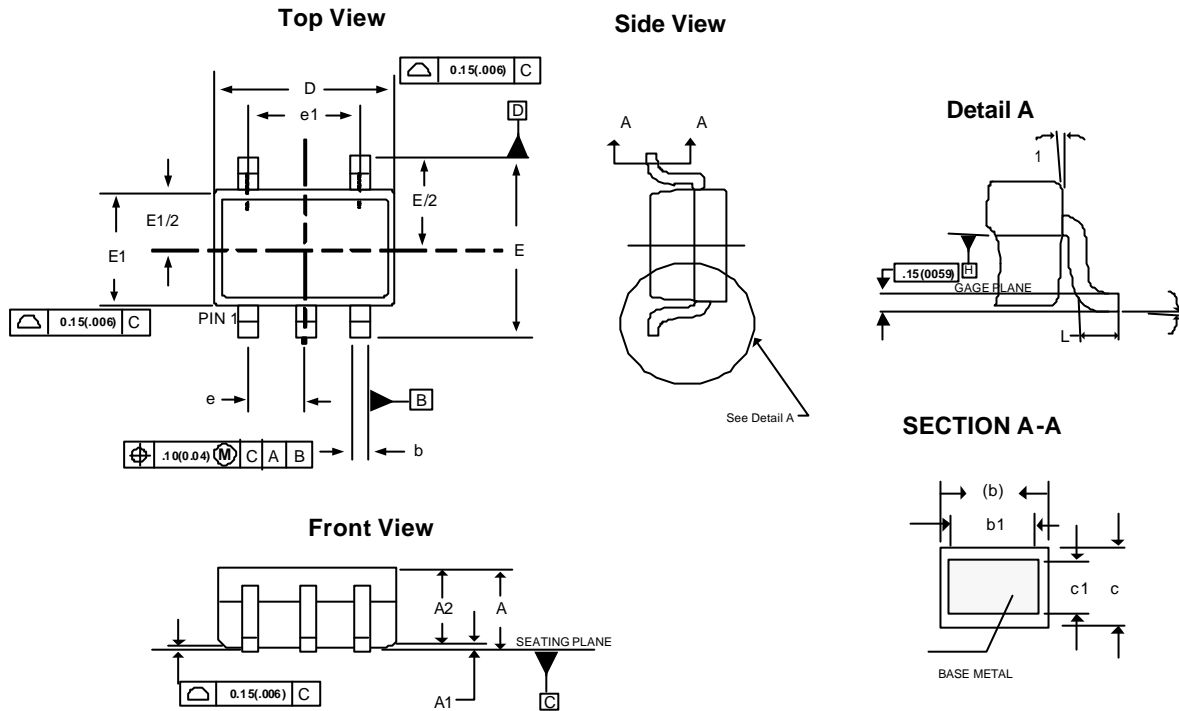
AME8853
■ Tape and Reel Dimension (Contd.)
SC-70-4 (SC-82)

Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
SC-70-4	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

AME8853

■ Package Dimension

SC-70-5

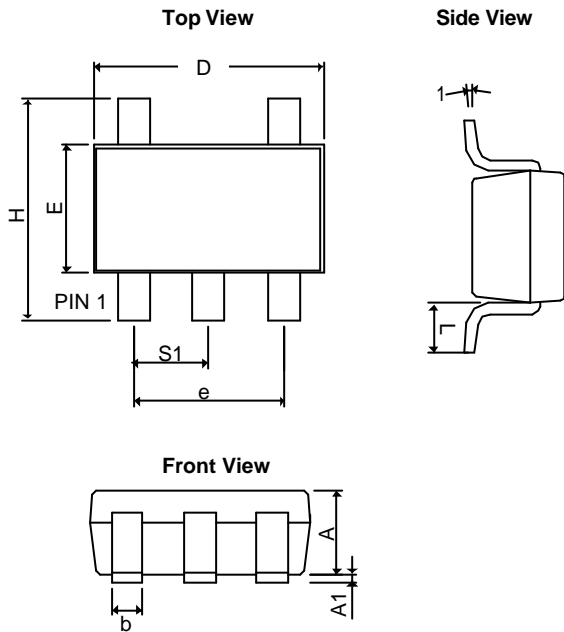


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.80	1.10	0.0315	0.0433
A1	0.00	0.10	0.0000	0.0039
A2	0.80	1.00	0.0315	0.0394
b	0.15	0.35	0.0059	0.0138
b1	0.15	0.25	0.0059	0.0098
c	0.08	0.25	0.0031	0.0098
c1	0.08	0.20	0.0031	0.0079
D	1.85	2.20	0.0728	0.0866
E	1.80	2.45	0.0709	0.0965
E1	1.10	1.40	0.0433	0.0551
e	0.65 BSC		0.0255 BSC	
e1	1.20	1.40	0.0472	0.0551
L	0.26	0.46	0.0102	0.0181
q1	0°	8°	0°	8°
q	4°	10°	4°	10°

AME8853

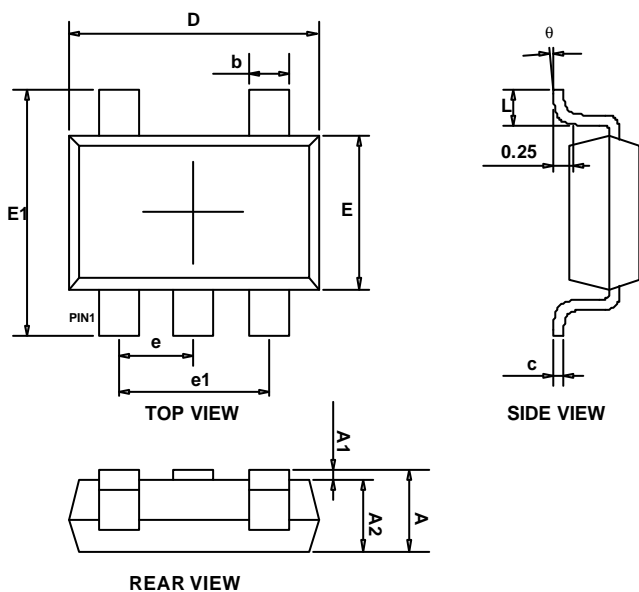
■ Package Dimension (Contd.)

SOT-25



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.30	0.0354	0.0512
A ₁	0.00	0.15	0.0000	0.0059
b	0.30	0.55	0.0118	0.0217
D	2.70	3.10	0.1063	0.1220
E	1.40	1.80	0.0551	0.0709
e	1.90 BSC		0.07480 BSC	
H	2.60	3.00	0.10236	0.11811
L	0.37BSC		0.0146BSC	
q1	0°	10°	0°	10°
S ₁	0.95BSC		0.0374BSC	

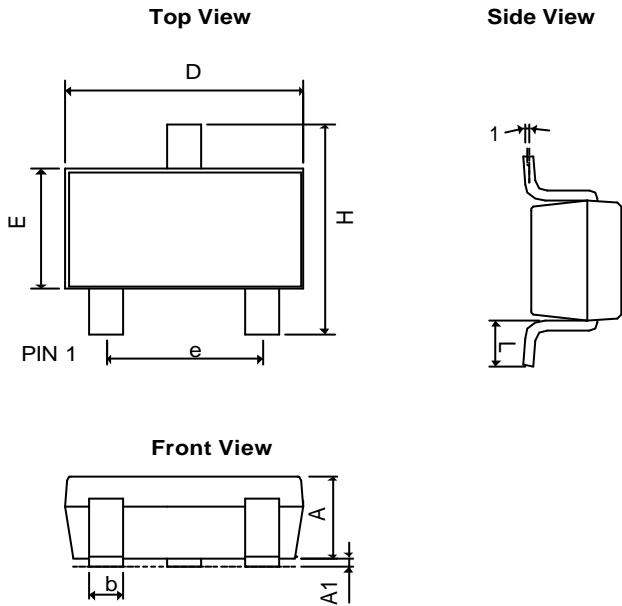
TSOT-25A



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.700	0.900	0.028	0.035
A ₁	0.000	0.100	0.000	0.004
A ₂	0.700	0.800	0.028	0.031
b	0.350	0.500	0.014	0.020
c	0.080	0.200	0.003	0.008
D	2.820	3.020	0.111	0.119
E	1.600	1.700	0.063	0.067
E ₁	2.650	2.950	0.104	0.116
e	0.95 BSC		0.037 BSC	
e ₁	1.90 BSC		0.075 BSC	
L	0.300	0.600	0.012	0.024
q	0°	8°	0°	8°

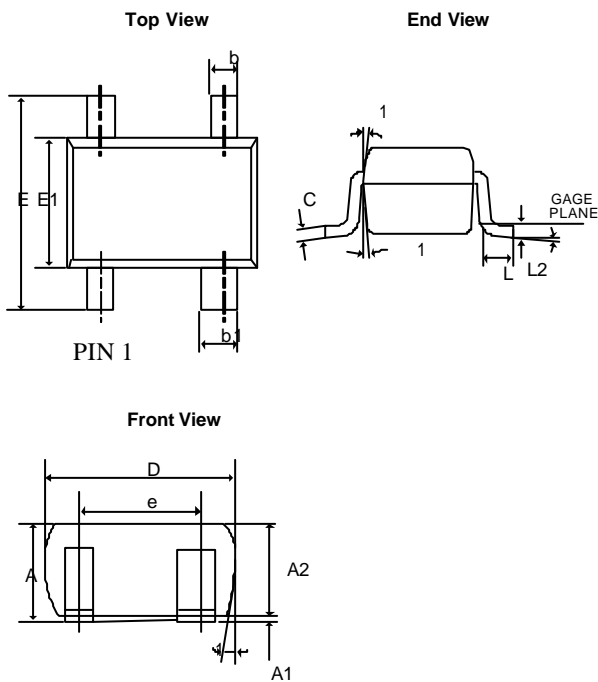
AME8853

SOT-23



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.40	0.0354	0.0551
A ₁	0.00	0.15	0.0000	0.0059
b	0.30	0.50	0.0118	0.0197
D	2.70	3.10	0.1063	0.1220
E	1.40	1.80	0.0551	0.0709
e	1.90 BSC		0.0748 BSC	
H	2.40	3.00	0.0945	0.1181
L	0.35BSC		0.0138 BSC	
q1	0°	10°	0°	10°

SC-70-4 (SC-82)

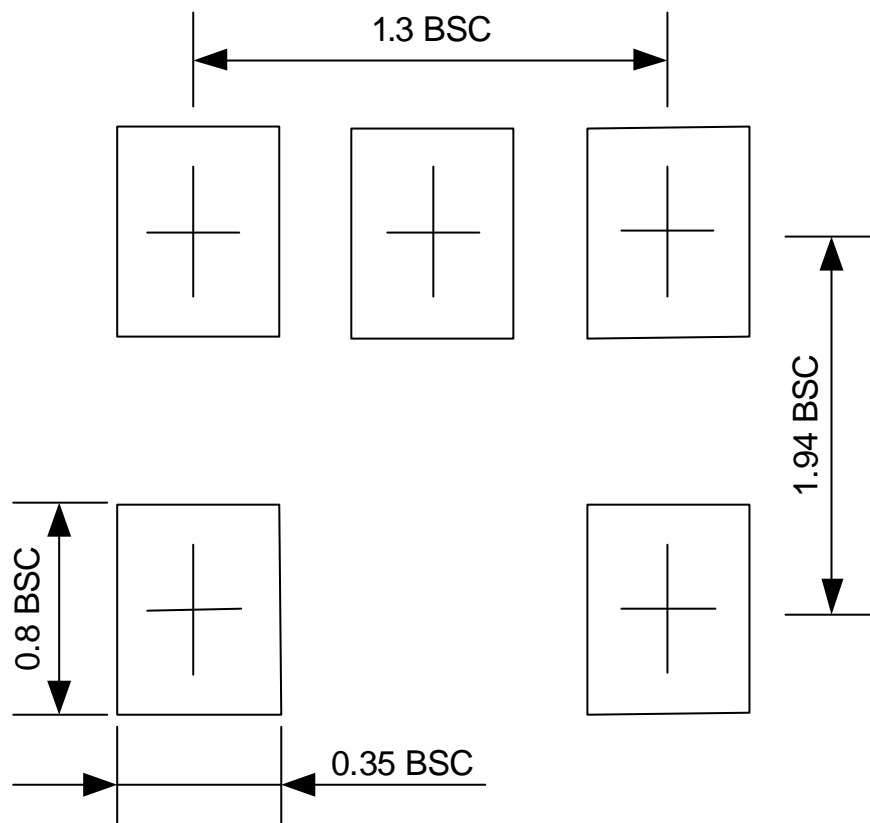


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	-	1.10	-	0.0433
A ₁	0.00	0.10	0.0000	0.0039
A ₂	0.70	1.00	0.0276	0.0394
b	0.25	0.35	0.0098	0.0138
b ₁	0.35	0.45	0.0138	0.0177
C	0.08	0.22	0.0031	0.0087
D	2.00 TYP		0.079 TYP	
E ₁	1.25 TYP		0.049 TYP	
E	2.10 TYP		0.083 TYP	
e	1.30 TYP		0.051 TYP	
L	0.26	0.46	0.0102	0.0181
L ₂	0.15 TYP		0.006 TYP	
y	-	0.10	-	0.0039
q	0°	8°	0°	8°
q1	4°	12°	4°	12°

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■ Lead pattern

SC-70-5



Note:

1. Lead pattern unit description:

BSC: Basic. Represents theoretical exact dimension or dimension target.

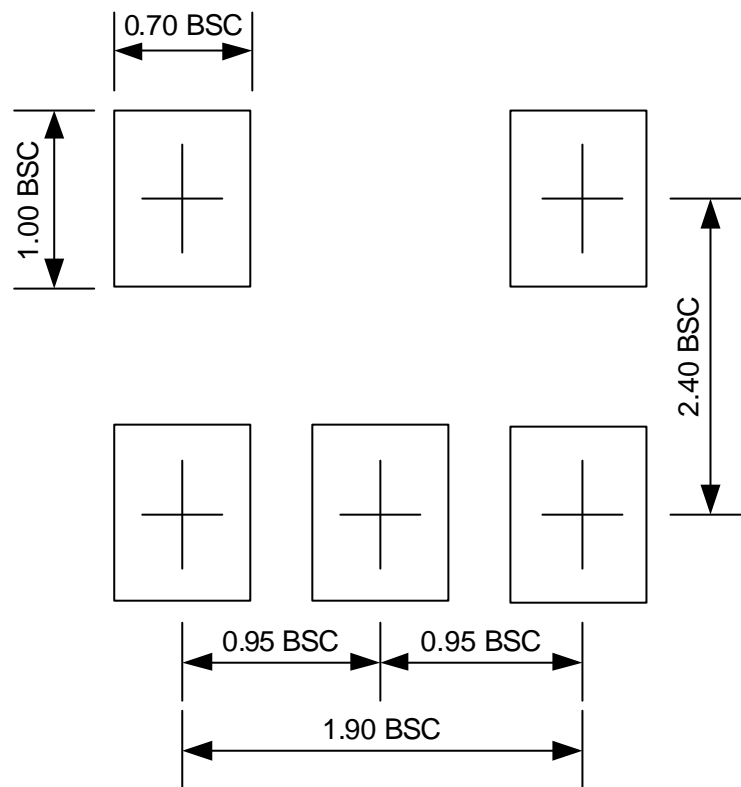
2. Dimensions in Millimeters.

3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified.

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■ Lead pattern

SOT-25



Note:

1. Lead pattern unit description:

BSC: Basic. Represents theoretical exact dimension or dimension target.

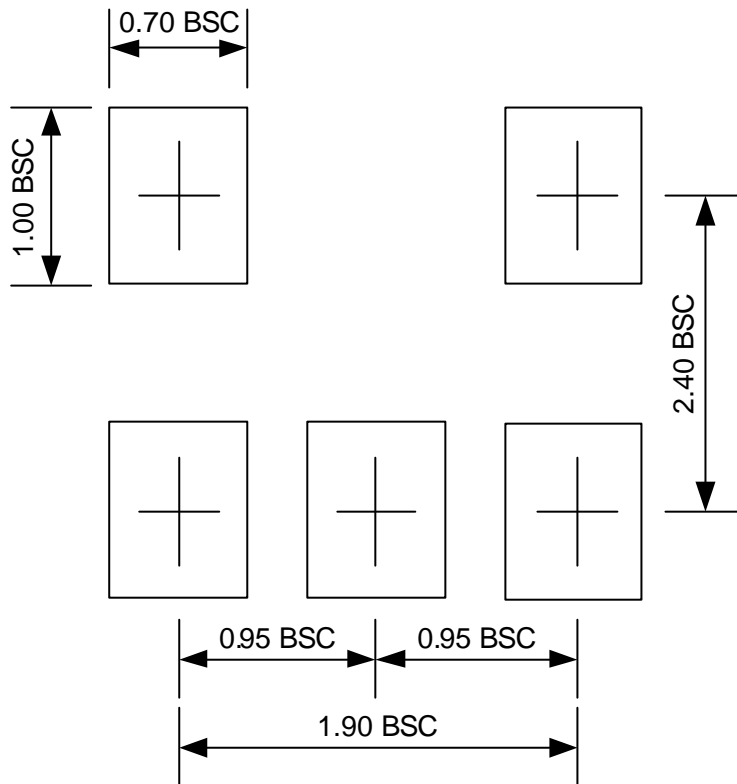
2. Dimensions in Millimeters.

3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified.

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■ Lead pattern

TSOT-25A



Note:

1. Lead pattern unit description:

BSC: Basic. Represents theoretical exact dimension or dimension target.

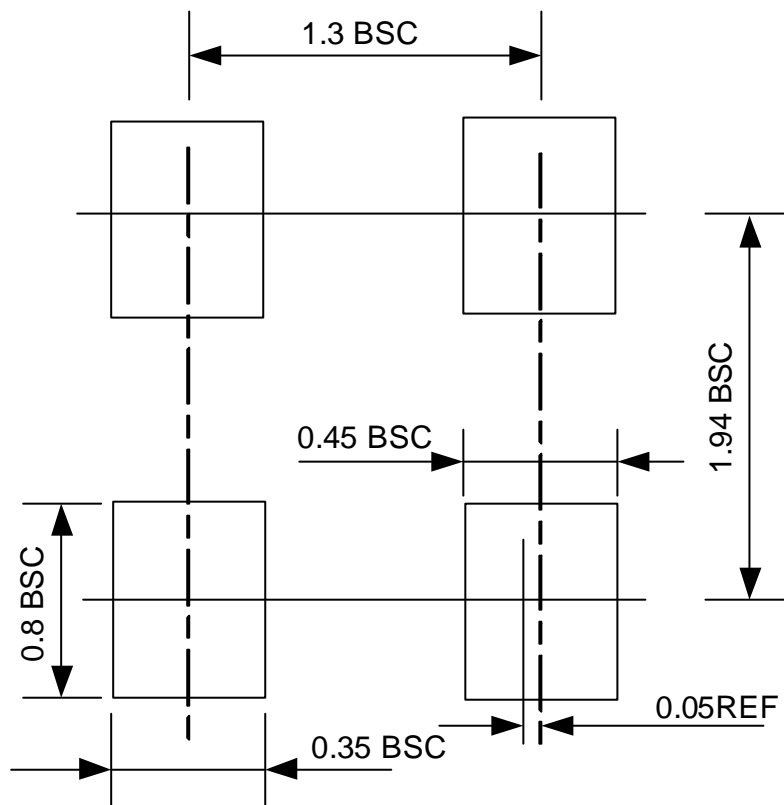
2. Dimensions in Millimeters.

3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified.

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■ Lead pattern

SC-70-4/SC-82



Note:

1. Lead pattern unit description:

BSC: Basic. Represents theoretical exact dimension or dimension target.

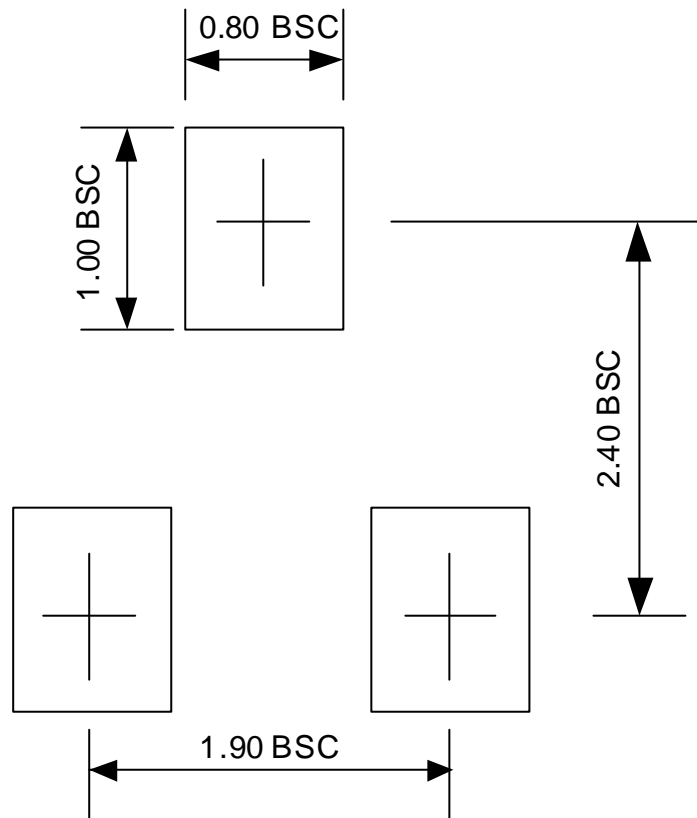
2. Dimensions in Millimeters.

3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified.

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■ Lead pattern

SOT-23



Note:

1. Lead pattern unit description:

BSC: Basic. Represents theoretical exact dimension or dimension target.

2. Dimensions in Millimeters.

3. General tolerance $\pm 0.05\text{mm}$ unless otherwise specified.



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